

AMENDMENT TO THE SPECIFICATION

The correction of two minor typographic errors is requested in the specification as follows.

[0025] A DC link is coupled to digital ballast 140. Digital ballast 140 includes integrated circuit IC2, which can be a digital controller, microcomputer, or microprocessor. IC2 can be, for example, the single chip controller FMS7401 from Fairchild Semiconductor. In some embodiments digital controller IC2 may contain digital control blocks including frequency changeable two-output PWM with a dead time control block, an Analog-to-Digital converter, analog and digital multiplexed input/output ports, an internal EEROM, flash ROM, and RAM memories. The functions of IC2 will be described below in detail.

[0026] IC2 is coupled to power device driver IC3. In the embodiment of FIG. 2, output pins 10, 11, and 13 of IC2 are coupled to input pins 12, 10, and 9 of IC3, respectively. In other embodiments input and output pins can be coupled differently. IC3 is coupled to an output stage, which contains power devices Q2 and Q3. Power devices Q2 and Q3 are coupled in series between the DC link, or a high voltage rail, and the ground. Power devices Q2 and Q3 can be bipolar junction transistors or any type of MOS-FET or IGBT devices. In embodiments with bipolar transistors power devices driver IC3 is coupled to the base of the bipolar transistors. In embodiments with MOS-FET power devices, IC3 is coupled to the gates of the MOS-FET power devices.